

**IN THE CLAIMS**

1. (Currently Amended) A device comprising:  
a first substrate coupled to a second substrate;  
the first substrate comprising a plurality of display blocks which are deposited onto said first substrate and an integrated circuit coupled to the display blocks, each of said plurality of display blocks fabricated separately and then deposited onto recessed regions of said first substrate;  
the integrated circuit, configured to receive a signal from an external source; and  
less than or equal to two [[a single]] I/Os coupled to at least one display block and said integrated circuit.
2. (Original) The device of claim 1, wherein the second substrate comprises one of a flexible layer and a rigid layer.
3. (Original) The device of claim 1, wherein the integrated circuit comprises at least one driver block deposited onto the first substrate, wherein the driver block is coupled to at least one display block.
4. (Original) The device of claim 1, wherein each of said shaped display blocks comprises an active circuit element which drives a picture element.
5. (Original) The device of claim 1, wherein the first substrate comprises an active matrix backplane, the device further comprises:  
a display generation substrate coupled to an active matrix backplane.
6. (Original) The device of claim 5, wherein the device has liquid crystal.

7. (Previously Presented) The device of claim 5, wherein the device has at least one OLED (Organic Light Emitting Diodes).
8. (Original) The device of claim 1, wherein said first substrate has an active matrix backplane which comprises at least one electrode for each picture element.
9. (Original) The device of claim 5, wherein said active matrix display is conformal.
10. (Original) The device of claim 1, wherein at least one of the first substrate and the second substrate is flexible.
11. (Currently Amended) An apparatus comprising:
  - at least one pixel block deposited onto a substrate, said at least one pixel block connected to a pixel element;
  - at least one interface block deposited onto said substrate;
  - said at least one pixel block and said at least one interface block electrically coupled to form an active matrix backplane;
  - wherein said at least one pixel block having at least one integrated circuit thereon;
  - wherein data are transferable to said at least one integrated circuit.
12. (Previously Presented) The apparatus of claim 11, wherein said at least one pixel block comprises an active circuit element which drives a picture element.
13. (Previously Presented) The apparatus of claim 11, further comprising:

a display generation substrate coupled to said active matrix backplane.

14. (Previously Presented) The apparatus of claim 11, wherein said active matrix display backplane comprises at least one electrode for each picture element.
15. (Previously Presented) The apparatus of claim 11, wherein said active matrix display panel is flexible and comprises a single crystal silicon transmissive display.
16. (Previously Presented) The apparatus of claim 11, wherein said active matrix display panel is flexible and comprises a single crystal silicon reflective display.
17. (Previously Presented) The apparatus of claim 11, wherein said active matrix display panel is flexible and comprises an organic light emitting diode.
18. (Previously Presented) The apparatus of claim 11, wherein said active matrix display panel is flexible and comprises upconverting phosphor.
19. (Currently Amended) A device comprising:
  - a first substrate;
  - a second substrate coupled to the first substrate;
  - the first substrate comprising a plurality of display blocks which are deposited onto said first substrate and an integrated circuit, said integrated circuit configured to receive a signal from an external source, each of said plurality of display blocks fabricated separately and then deposited onto recessed regions of said first substrate; and

less than or equal to four I/Os coupled to at least one display block and said integrated circuit.

20. (Currently Amended) A device comprising:

a first substrate;

a second substrate coupled to the first substrate;

the first substrate comprising a plurality of display blocks which are deposited onto said first substrate and an integrated circuit, said integrated circuit configured to receive a signal from an external source, each of said plurality of display blocks fabricated separately and then deposited onto recessed regions of said first substrate; and

less than or equal to three I/Os coupled to at least one display block and a chip that transmit information.